	A		
	Application No.	Applicant(s)	
Notice of Allamat illt.	10/673,081	JOYCE ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Tuan V. Thai	2186	
The MAILING DATE of this communication appeal all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED or other appropriate com IGHTS. This application is	in this application. If not included	'HIS nitiative
1. $oxed{\boxtimes}$ This communication is responsive to <u>amendment filed 8/16</u>	<u>6/2007</u> .		
2. X The allowed claim(s) is/are 1, 3-12 and 14-22 renumbered	as 1-20 respectively.		
Acknowledgment is made of a claim for foreign priority ur a) □ All b) □ Some* c) □ None of the:		I) or (f).	
1. Certified copies of the priority documents have			
2. Certified copies of the priority documents have			
 Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	cuments have been receiv	ed in this national stage application from t	the
	-641-1		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to the second section of this application.	lle a reply complying with the requirement	S
 A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 	itted. Note the attached E es reason(s) why the oath	XAMINER'S AMENDMENT or NOTICE Of or declaration is deficient.	F
 CORRECTED DRAWINGS (as "replacement sheets") must (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date 	son's Patent Drawing Revi	or in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the	.84(c)) should be written or he header according to 37 (the drawings in the front (not the back) of CFR 1.121(d).	
 DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT 	sit of BIOLOGICAL MA FOR THE DEPOSIT OF E	TERIAL must be submitted. Note the BIOLOGICAL MATERIAL.	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	E - Nakaa a	Informati Data at A. B. B.	
2. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)		Informal Patent Application Summary (PTO-413),	
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No	o./Mail Date 's Amendment/Comment	
Paper No./Mail Date			
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 		s Statement of Reasons for Allowance	
	9.	TUAN V. THAI PHIMARY EXAMINER	
		V(chood 2100	
ILS Potent and Tradement Office			

-Page 2-

Application/Control Number: 10/673,081

Art Unit: 2186

Attorney's Docket No.: 015114-066300US

IN THE UNITED STATES PATENT AND

TRADEMARK OFFICE

In re application of: Joyce et al. Group: 2186

Serial No.: 10/673,081 Examiner: Tuan Thai

For: TECHNIQUES FOR SEQUENTIALLY TRANSFERRING DATA FROM A

MEMORY DEVICE THROUGH A PARALLEL INTERFACE.

1. This action is responsive to communication filed August 16, 2007. Claims 1, 2-12 and 14-22 are now allowed. Claims 2 and 13 have been canceled.

REASONS FOR ALLOWANCE

2. The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record do not teach nor suggest, either alone or in combination, all the limitations of the amended claims of the current invention (claims 1 and 12). The discussion of the reasons for allowance shall be directed to claim 1 in which the Examiner shall designate as the primary invention in this application; however, the reasons for allowance will also apply to other indicated independent claim (claim 12).

Application/Control Number: 10/673,081

Art Unit: 2186

Particularly, the prior arts of record do not teach nor suggest all the combined limitations in each claim separately including a method for transferring data in parallel from an external memory device to an integrated circuit comprises transferring a start read address from the integrated circuit via data lines to the external memory device; providing a clock signal generated by the integrated circuit to the external memory device; sequentially generating read addresses in response to the clock signal beginning with the start read address using an address counter in the external memory device; reading data stored in the external memory device at the read addresses; transferring the data in parallel from the external memory device to the integrated circuit; resetting a read address to the start read address when an output enable signal generated by the integrated circuit has a first voltage, wherein the address counter increments the read address in response to the clock signal when the output enable signal has a second voltage; similarly for other independent claim 12. In light of the foregoing, claims 1 and 12 of the present application are found to be patentable over the prior arts.

Claims 3-6 and 14-22 further limit the allowable independent claims 1 and 12. These claims are therefore allowable for the same reason as set forth above.

Any comments considered necessary by Applicant must be

Application/Control Number: 10/673,081

Art Unit: 2186

submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan V. Thai whose telephone number is (571)-272-4187. The examiner can normally be reached on from 6:30 A.M. to 3:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew M. Kim can be reached on (571)-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Application/Control Number: 10/673,081

-Page 5-

Art Unit: 2186

TVT/September 25, 2007

Tuan V. Tha

PRIMARY EXAMINER

Group 2100